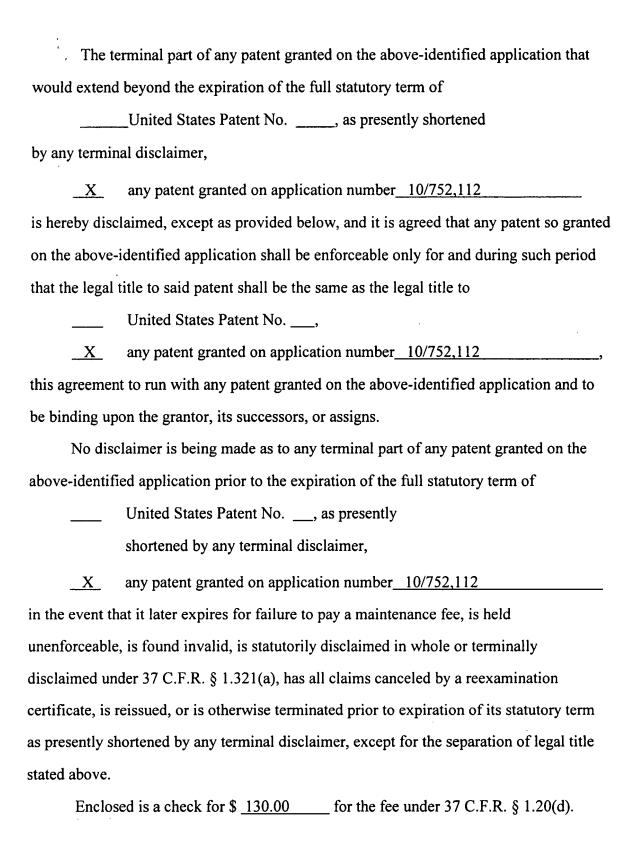


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:	
Hae-Seung Lee et al.	I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelop addressed to the Commissioner for Patents, P.O. Bo 1450, Alexandria, VA 22313-1450.
Application No.: 10/752,131	
Filed: January 6, 2004	
Art unit: 2609	on 7/27/07 Date of Deposit
Examiner: Hsu, Amy R	Name of Person Majling Confespondence
For: CMOS ACTIVE PIXEL WITH HARD AND SOFT RESET	Signature Date
Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450 TERMINAL DISCLAIMER UN	DER 37 C.F.R. § 1.321(c)
The undersigned attorney represents that the undersigned attorney is the attorney	
of record for the above-referenced patent application.	
The assignee of the entire right, title, and interest in and to the above-referenced	
patent application is <u>Cypress Semiconductor Co</u> (Nam	rporation ("assignee"), le of Assignee)
a <u>Delaware</u> corporation having (State of Incorporation)	a place of business at
198 Champion Court, San Jose, California 9513 (Address	



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Dated: <u>7/27/07</u>	Respectfully submitted, BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP By: Name: Daniel E.Ovanezian (Type or print)
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hereby certify that this correspor class mail with sufficient postage Alexandria, Virginia 22313-1450. on	ndence is being deposited with the United States Postal Service as first in an envelope addressed to Commissioner for Patents, PO Box 1450,
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